

REMARKS

The claims are claims 1 and 2.

The application has been further amended at many locations to correct minor errors and to present uniform language throughout. Table 1 has been corrected to include text left out of one of the cells. This subject matter is disclosed at page 28, line 21. These amendments include correction of a character rendering error in Table 2 and a similar error in the application sequences listed at pages 52 and 53. The final paragraph of this application has been amended to correspond to the reference numbers illustrated in Figure 13.

Claim 1 has been amended in response to the rejection under 35 U.S.C. 112 and the objection under 37 CFR 1.75(d)(1).

Claim 1 was rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. The Examiner states that the phrases "said function clock circuit" of line 23 and "said phase locked loop clock circuit" of line 26 lack antecedent basis.

The Applicant respectfully submits that the phrase "said functional clock circuit" has antecedence from the recitation of original line 4, current line 5. The phrase "said phase locked loop clock circuit" of original line 26 has been amended to read "said clock circuit." This phrase finds antecedence from original and current line 16. Accordingly, claim 1 is now proper under 35 U.S.C. 112.

Claim 1 has been amended to correct the grammatical mistakes noted by the Examiner. According, claim 1 is now proper under 37 CFR 1.75(d)(1).

The Applicants respectfully submit that all the present claims are allowable for the reasons set forth above. Therefore early reconsideration and advance to issue are respectfully requested.

If the Examiner has any questions or other correspondence regarding this application, Applicants request that the Examiner contact Applicants' attorney at the below listed telephone number and address to facilitate prosecution.

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Respectfully submitted,

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